

Fig. 1A
SIDE VIEW

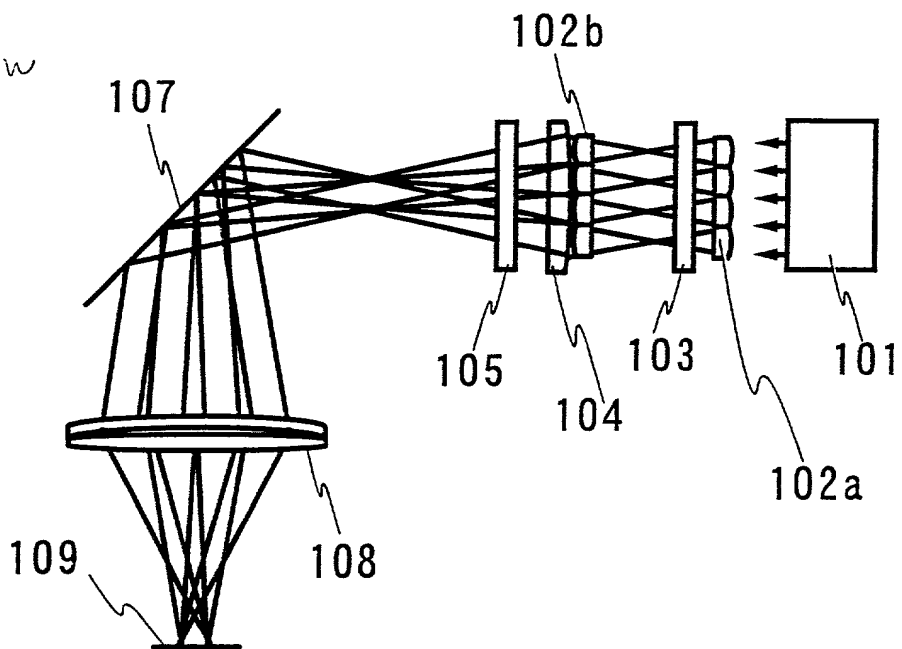
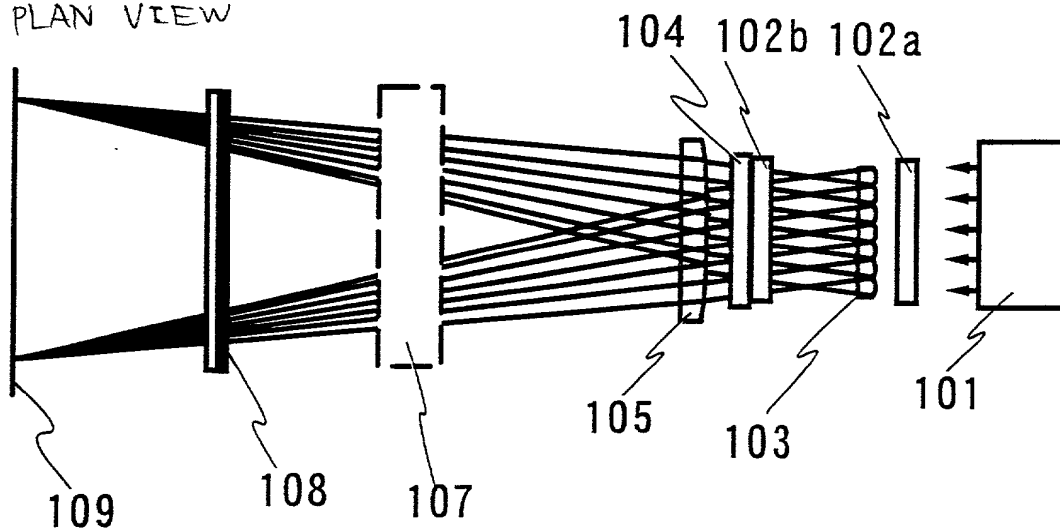


Fig. 1B
PLAN VIEW



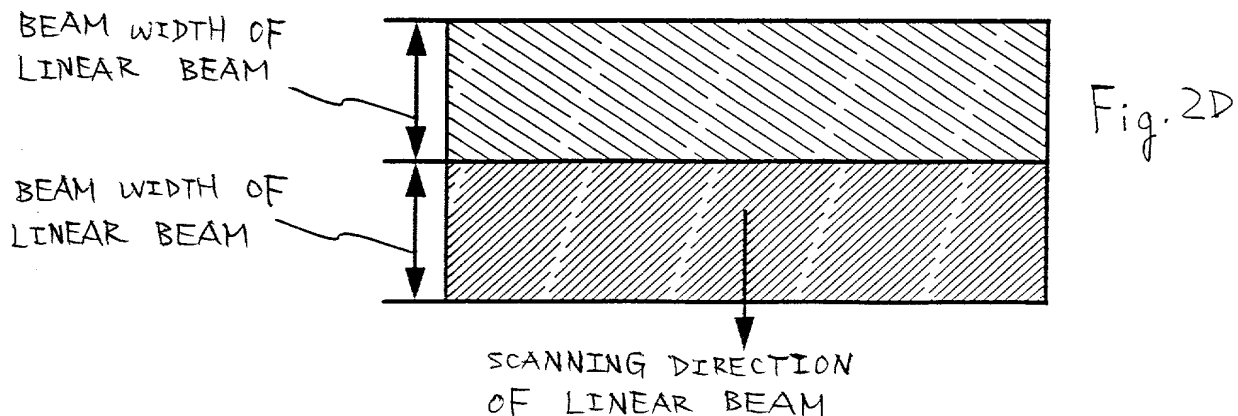
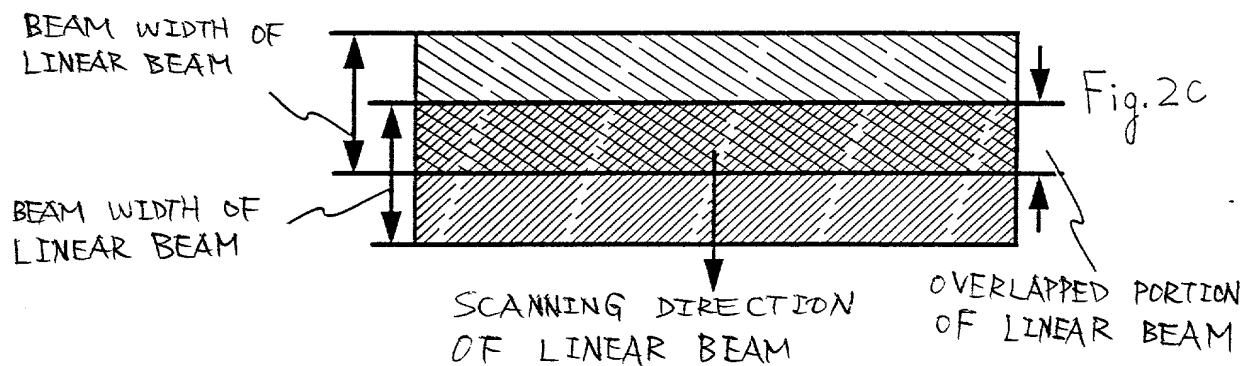
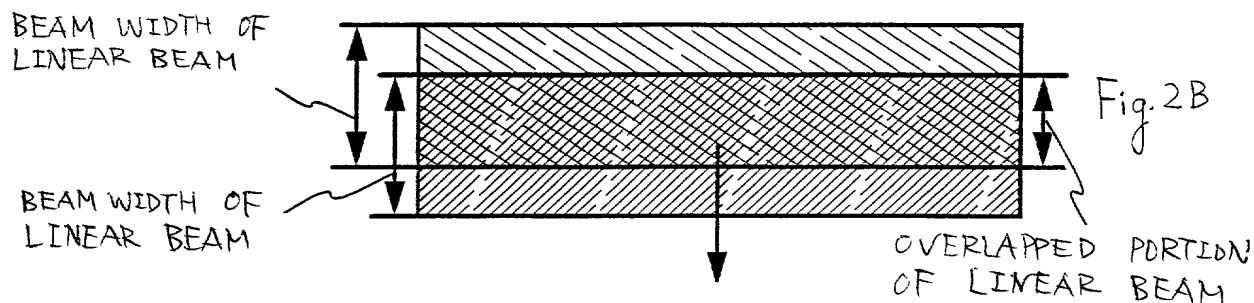
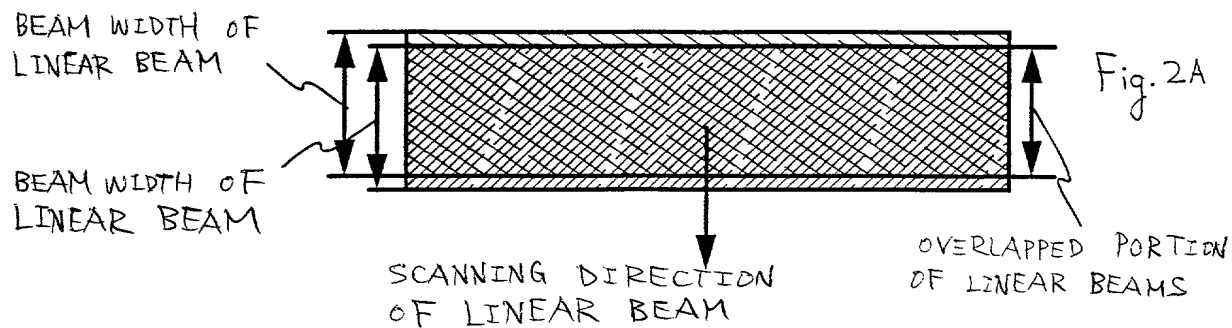


Fig. 3

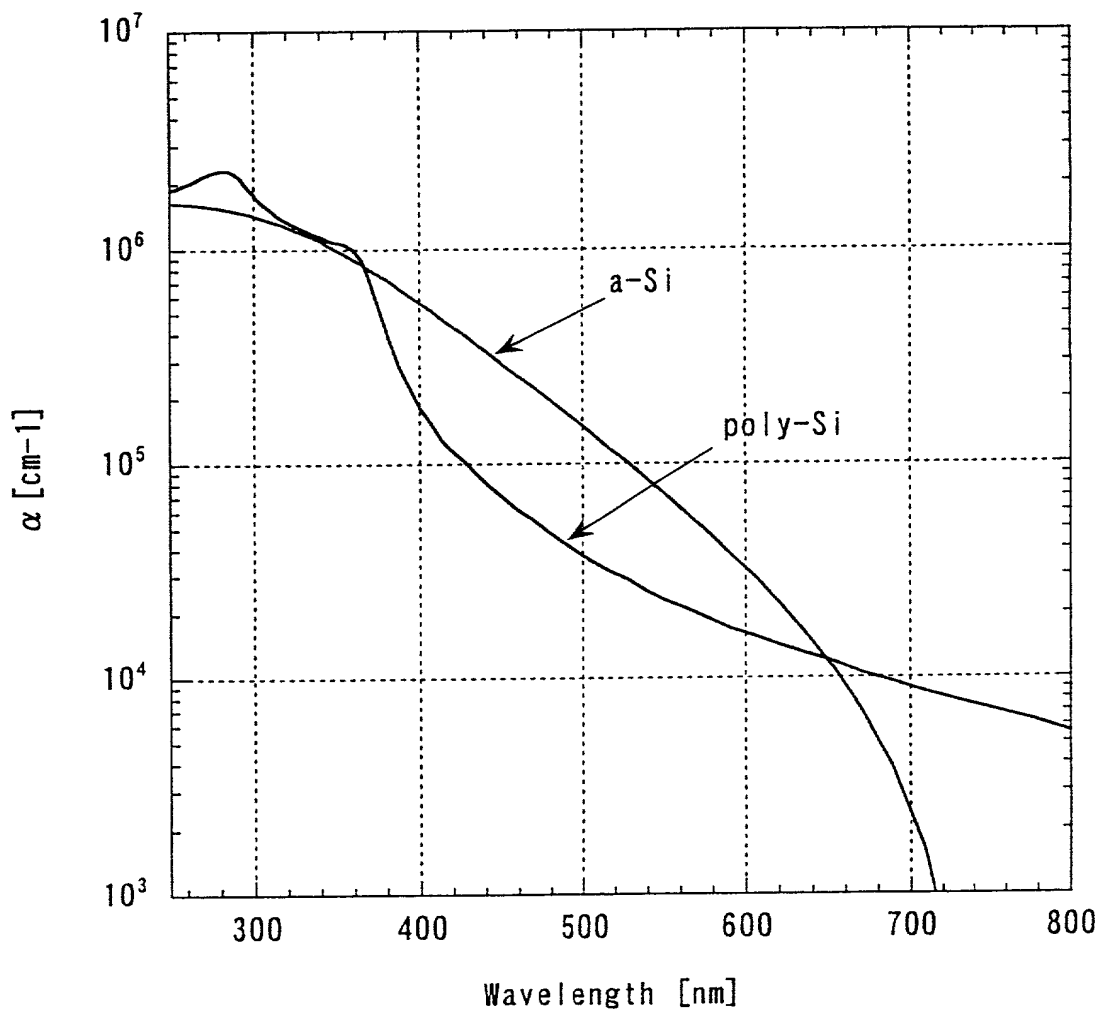


Fig. 4A

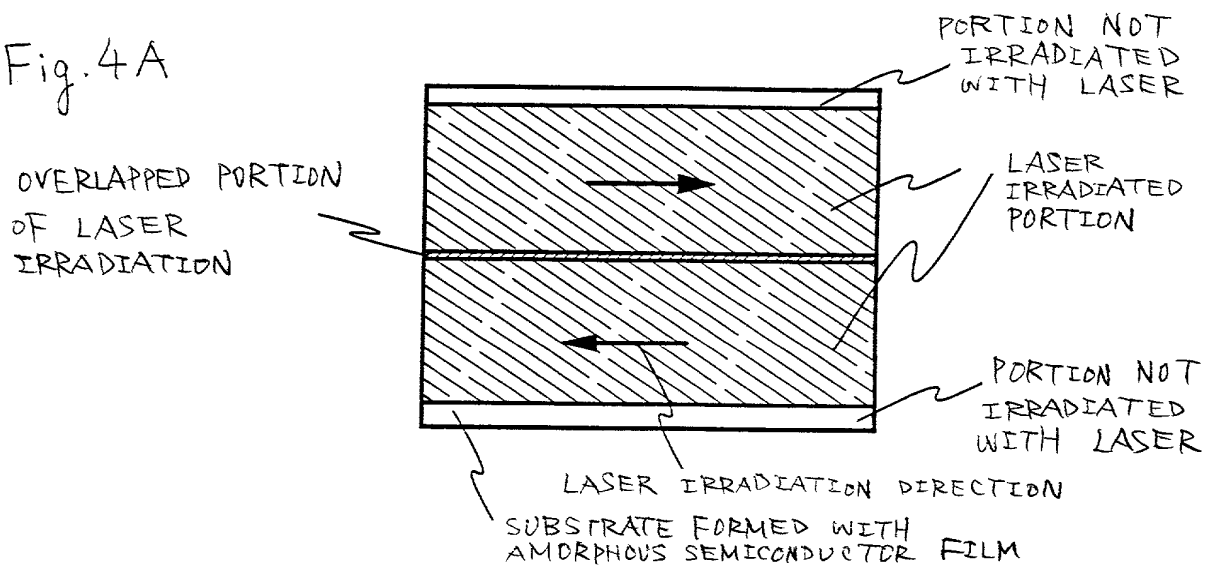


Fig. 4B

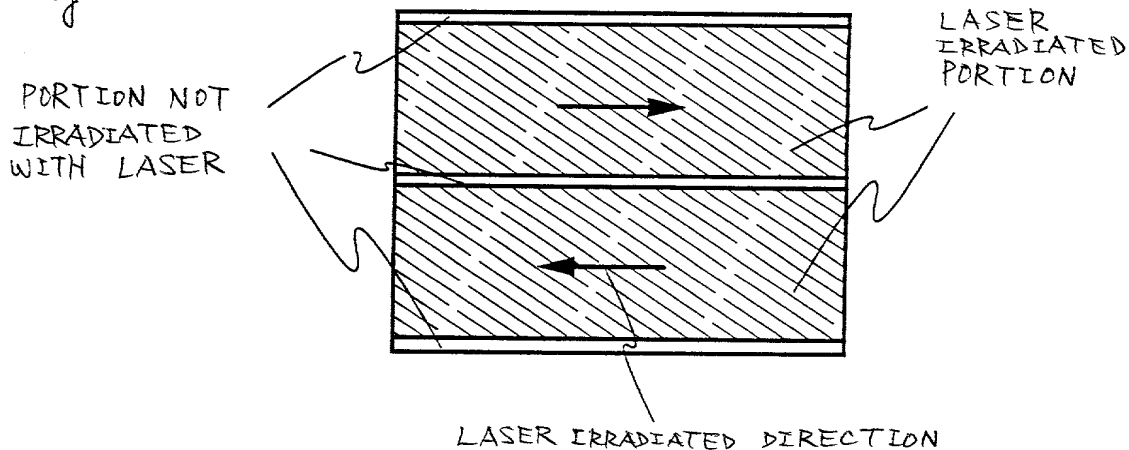


Fig. 4C

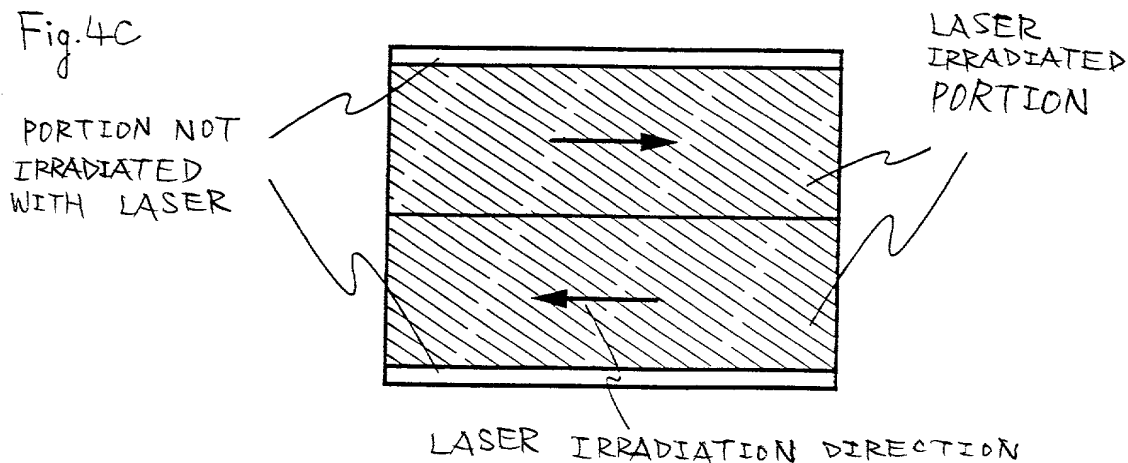


Fig. 5A

SIDE VIEW

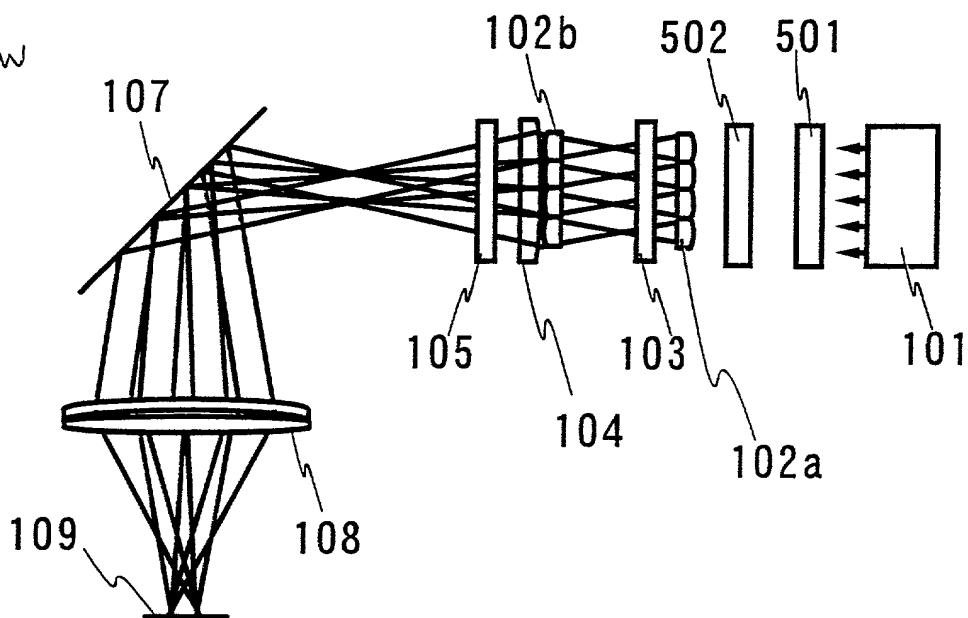


Fig. 5B

PLAN VIEW

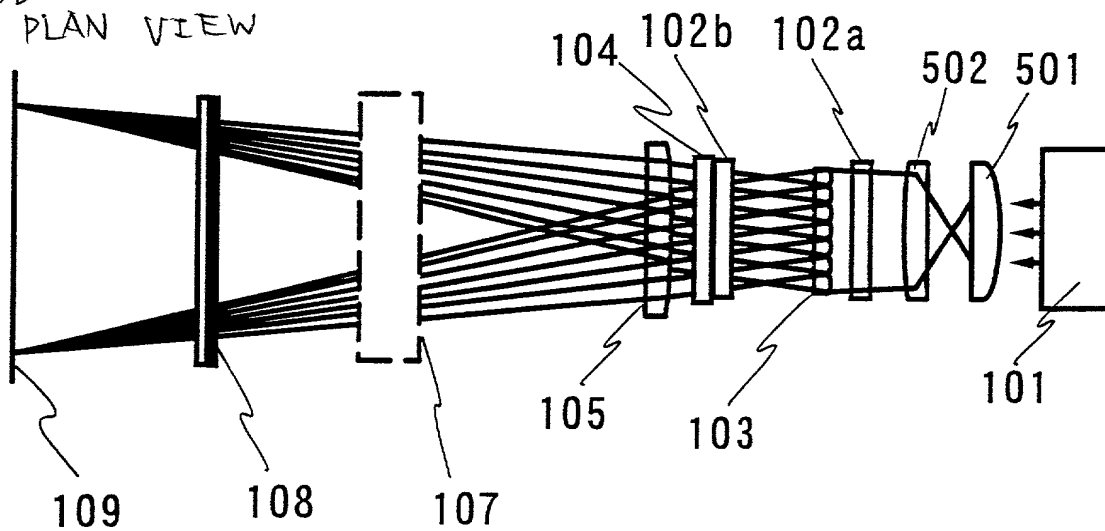
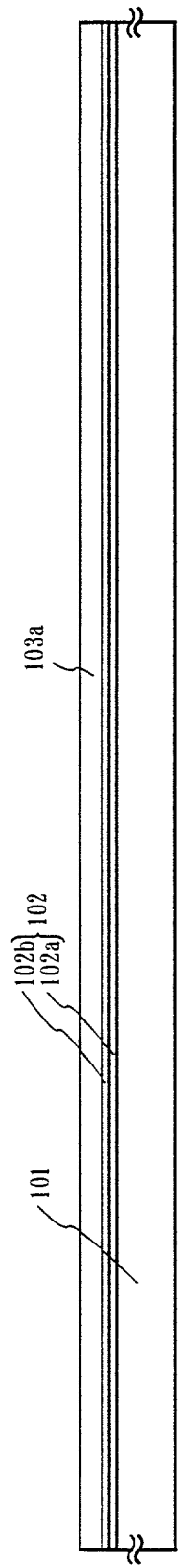


FIG. 6A

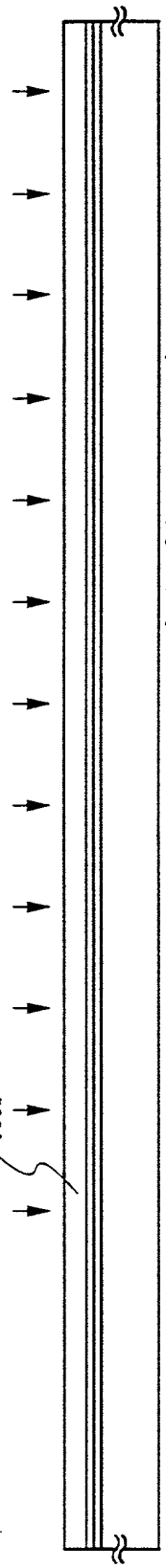
FORMATION OF UNDERLYING FILM AND AMORPHOUS SEMICONDUCTOR FILM

Fig. 6A



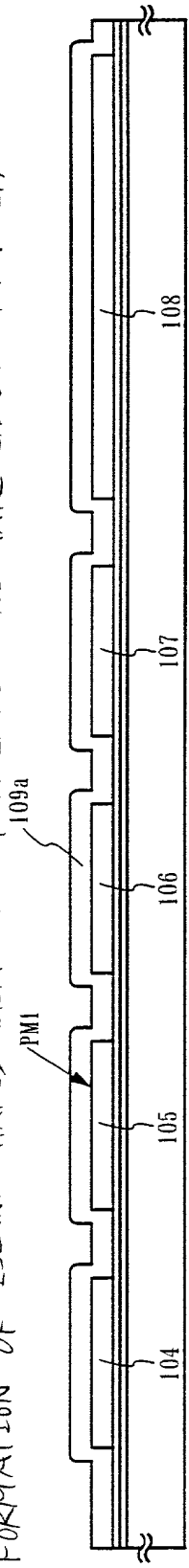
CRYSTALLIZATION PROCESS

Fig. 6B



FORMATION OF ISLAND-SHAPED SEMICONDUCTOR LAYER AND GATE INSULATING FILM

Fig. 6C



FORMATION OF HEAT-RESISTANT CONDUCTIVE LAYER

Fig. 6D

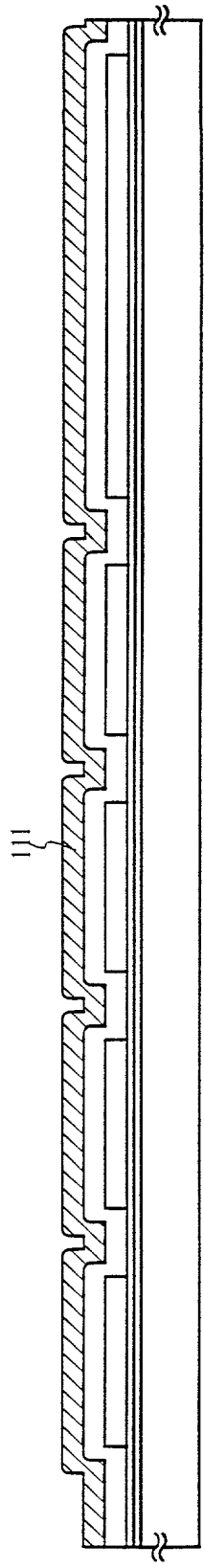
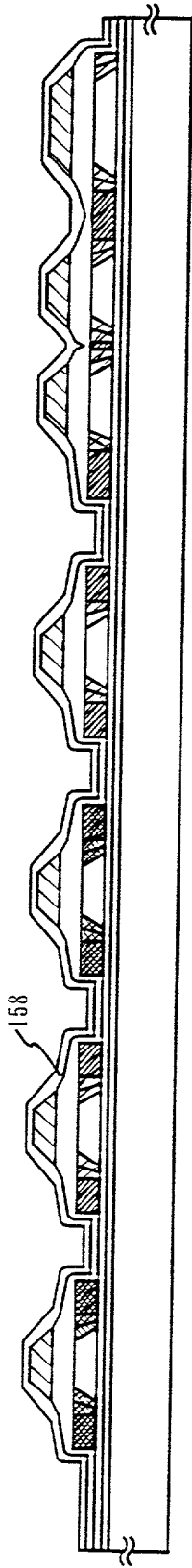


Fig. 8A FORMATION OF FIRST INTERLAYER INSULATING FILM



FORMATION OF SECOND INTERLAYER INSULATING FILM AND CONTACT HOLE
FIG. 8B AND WIRING AND PIXEL ELECTRODE

B
8
Fig

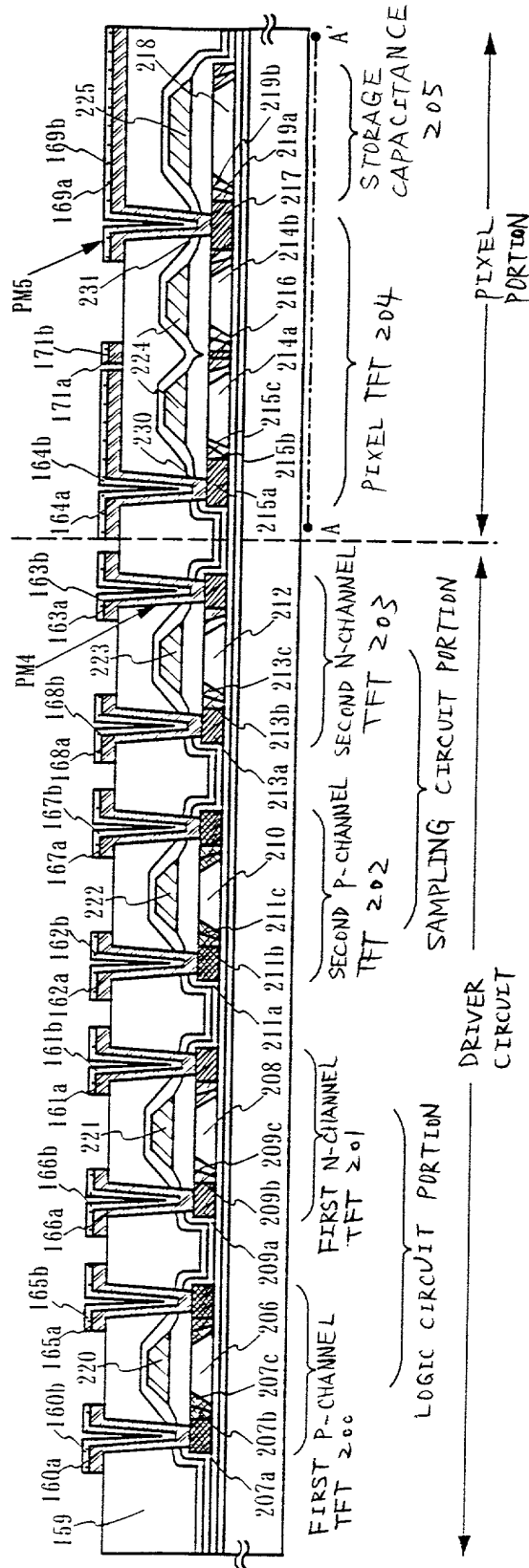
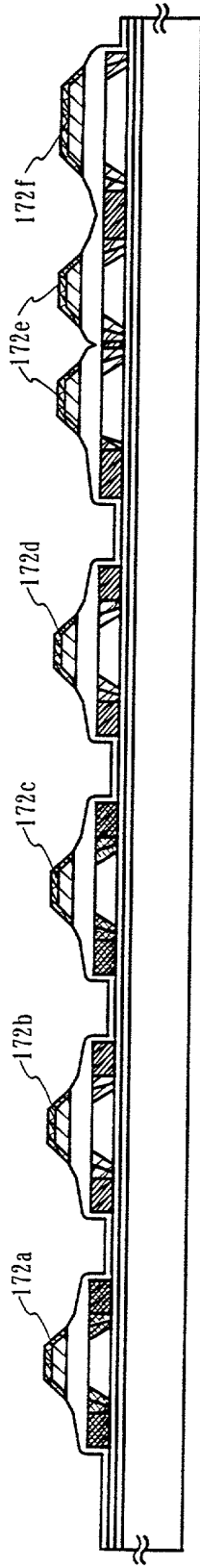
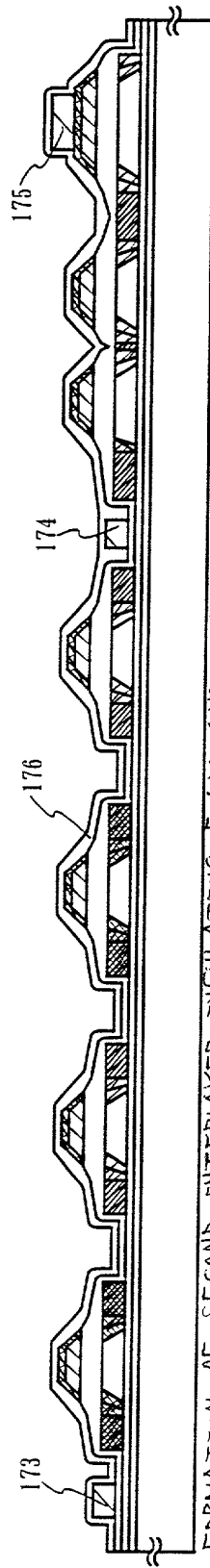


Fig. 9A



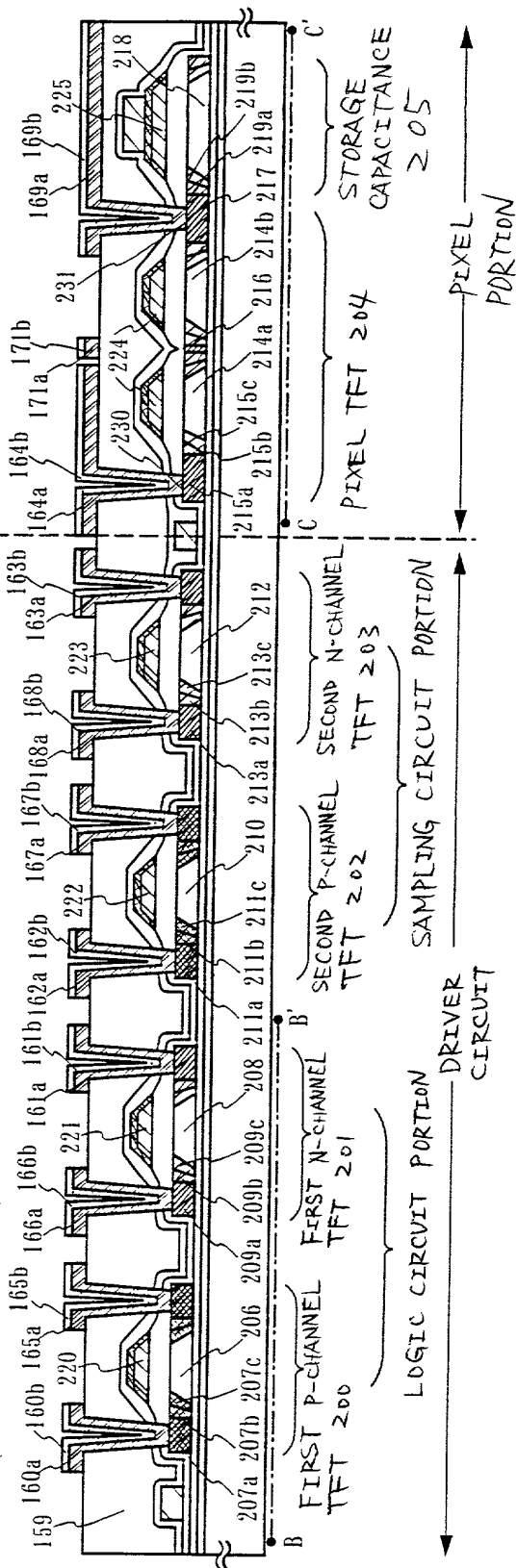
FORMATION OF FIRST INTERLAYER INSULATING FILM

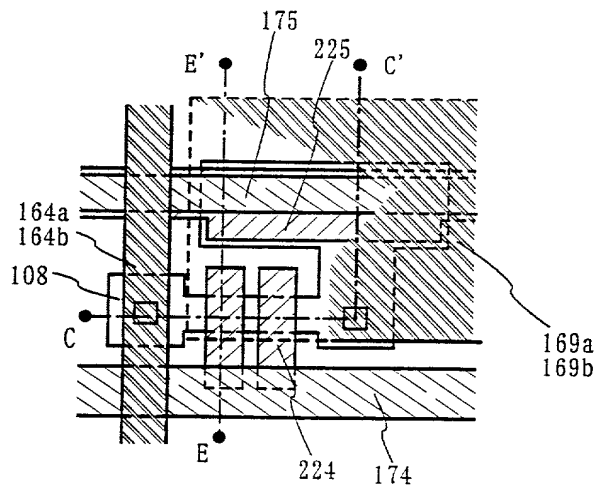
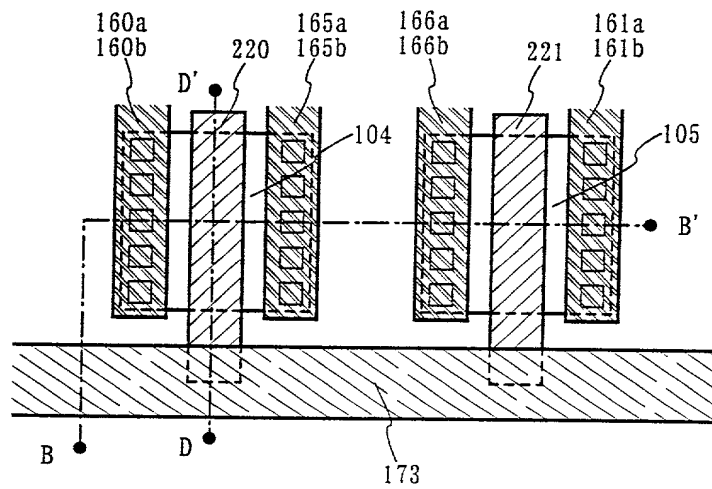
Fig. 9B



FORMATION OF SECOND INTERLAYER INSULATING FILM AND CONTACT HOLE AND WIRING

Fig. 9C





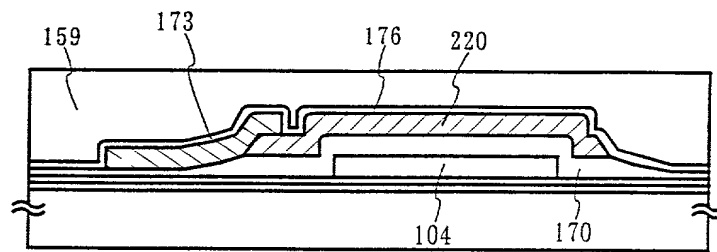


Fig. 11A

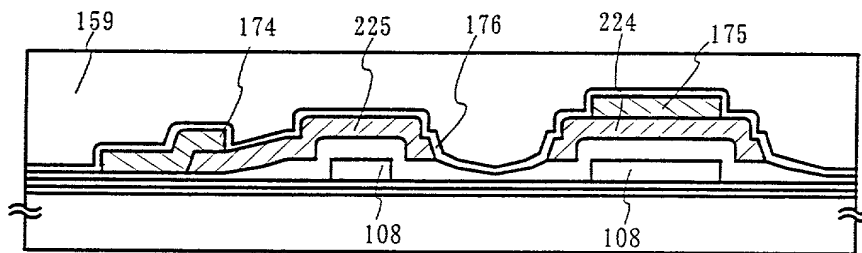
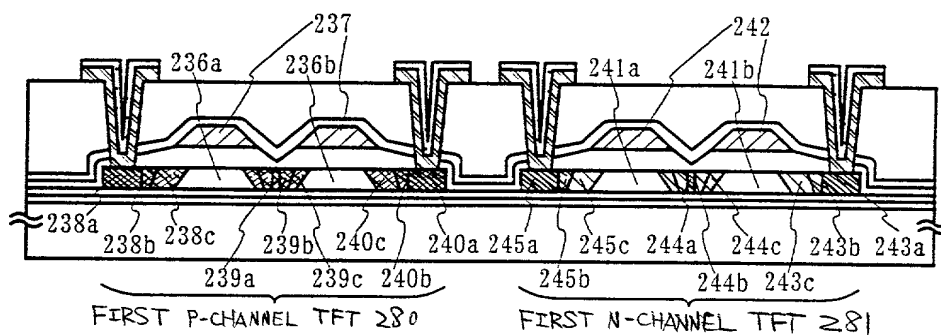
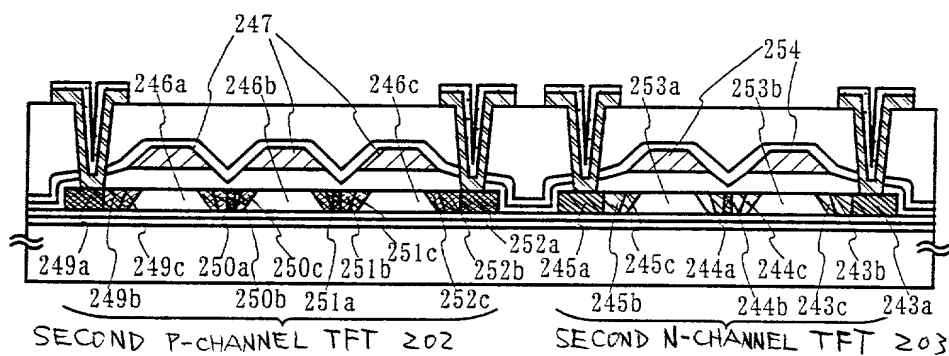


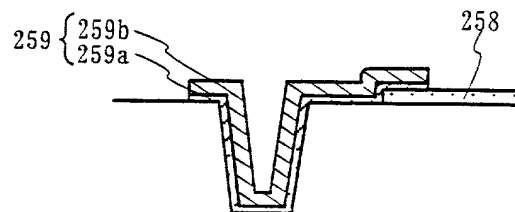
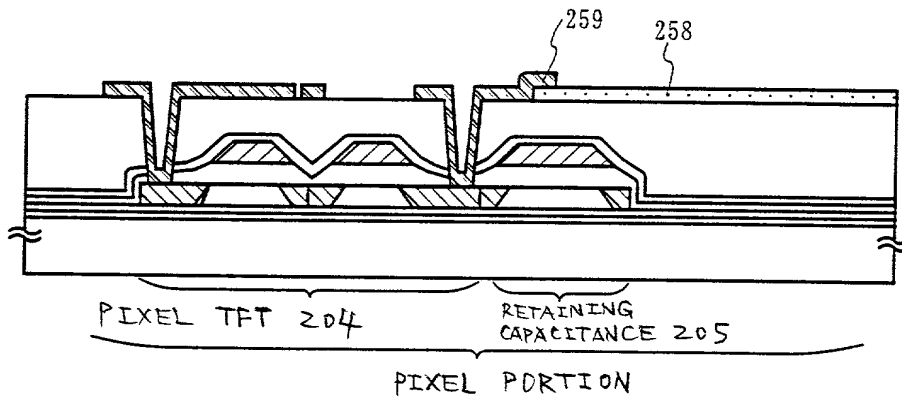
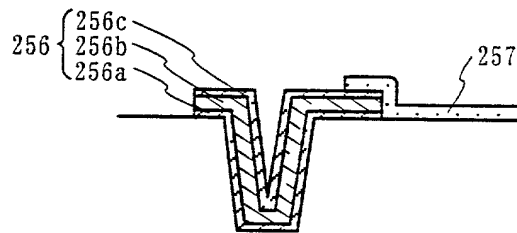
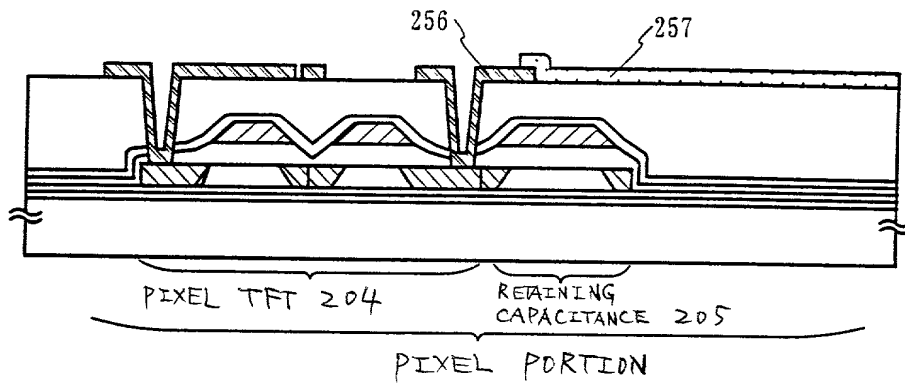
Fig. 11B



LOGIC CIRCUIT PORTION



SAMPLING CIRCUIT PORTION



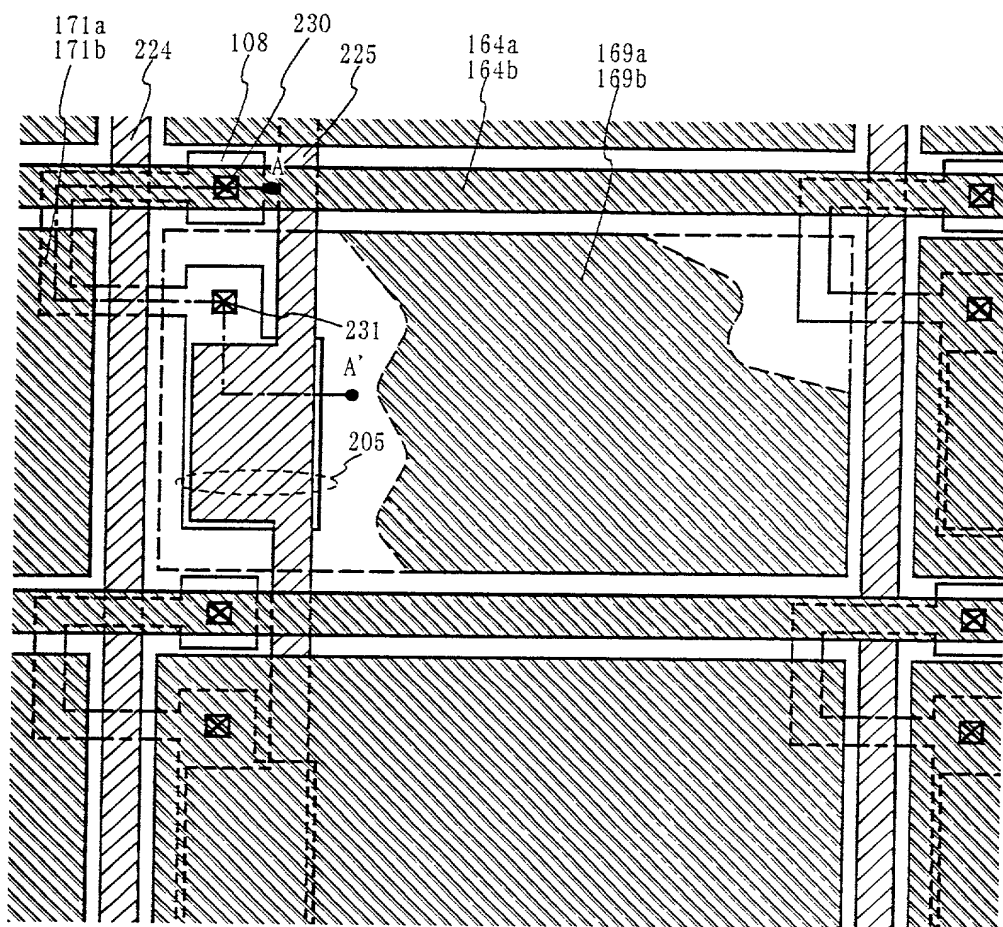
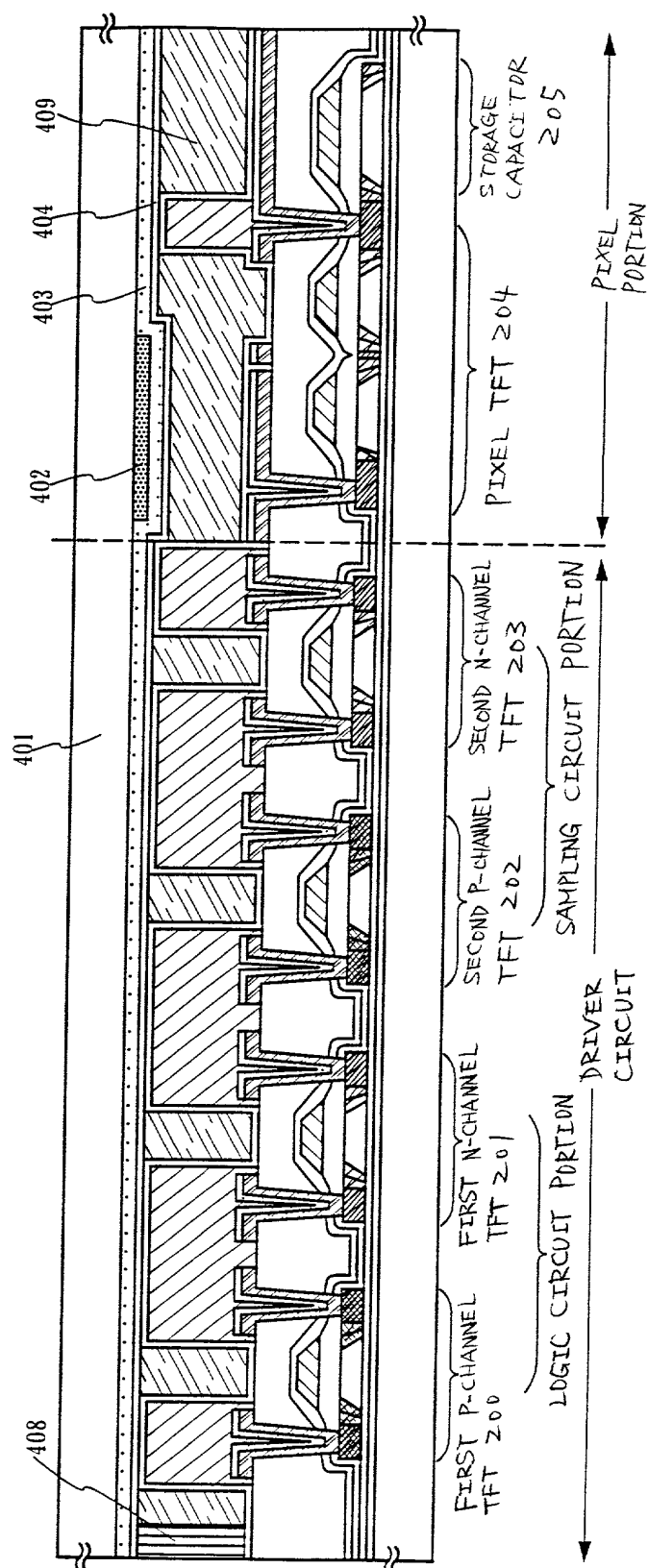
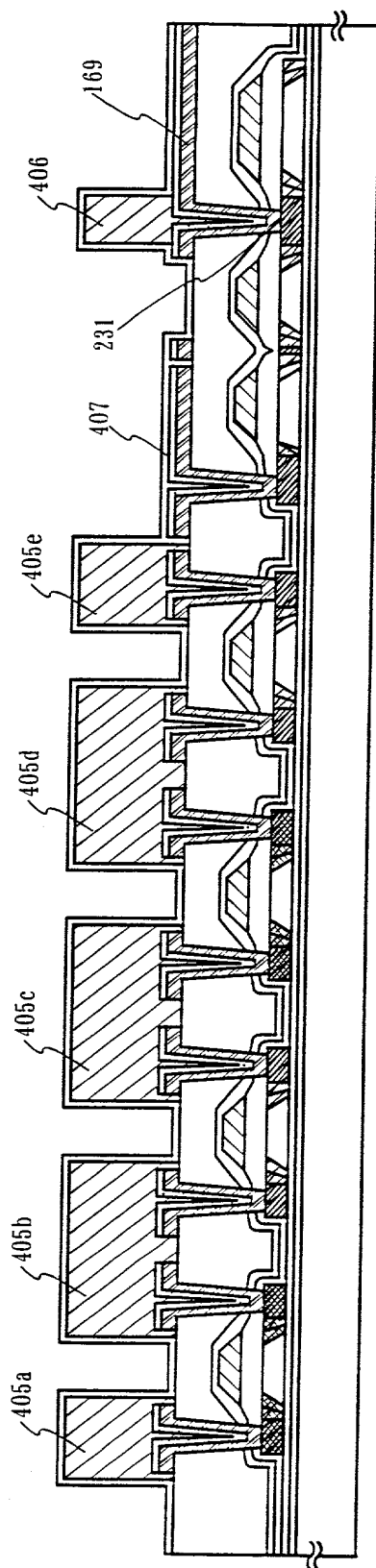


Fig. 14



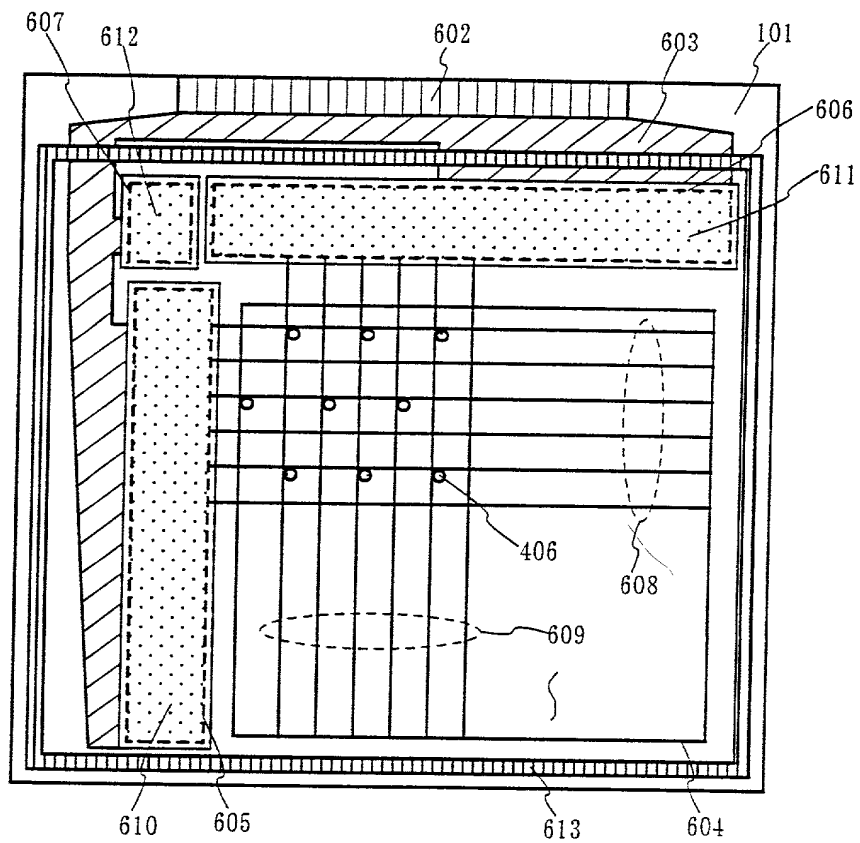


Fig. 16

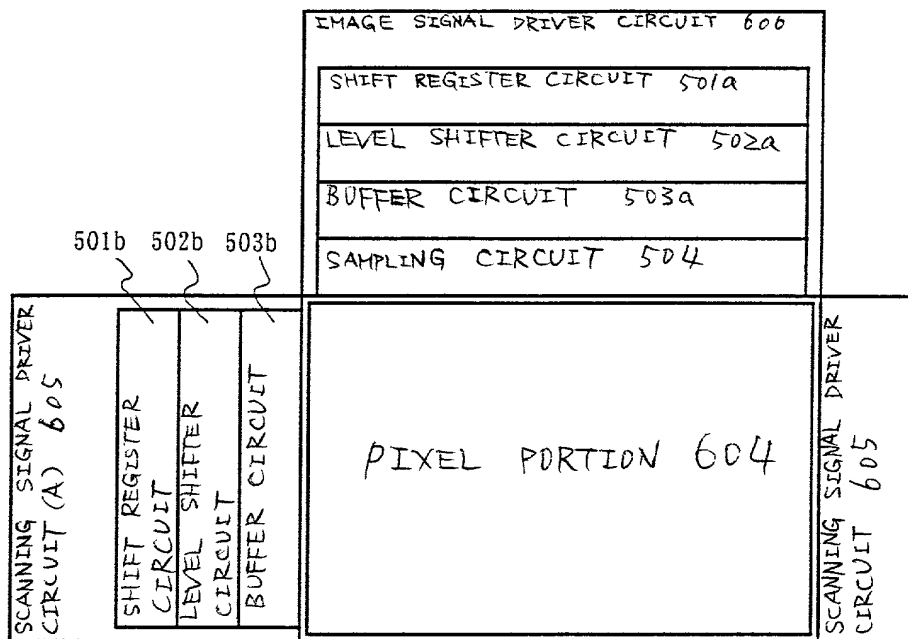


Fig. 18

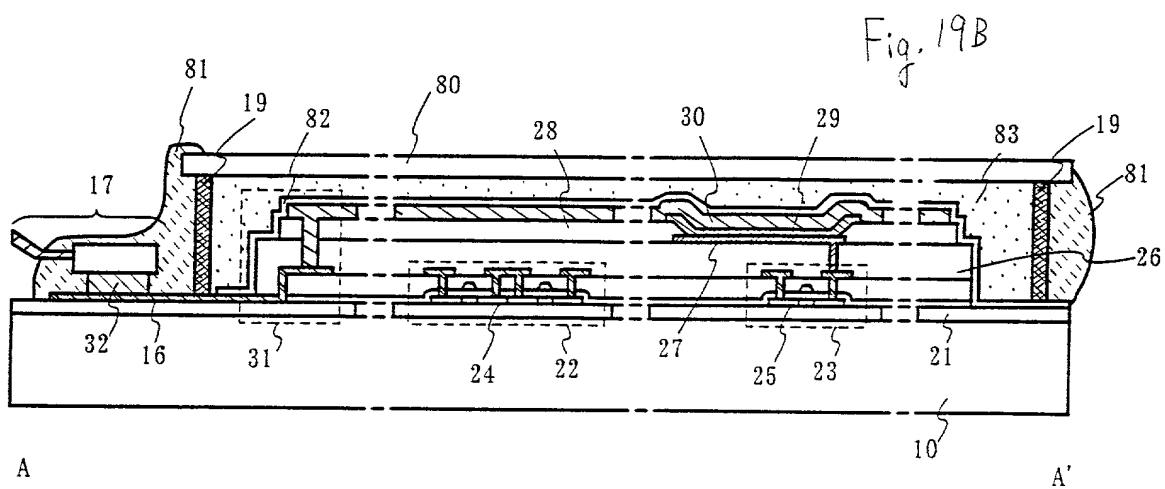
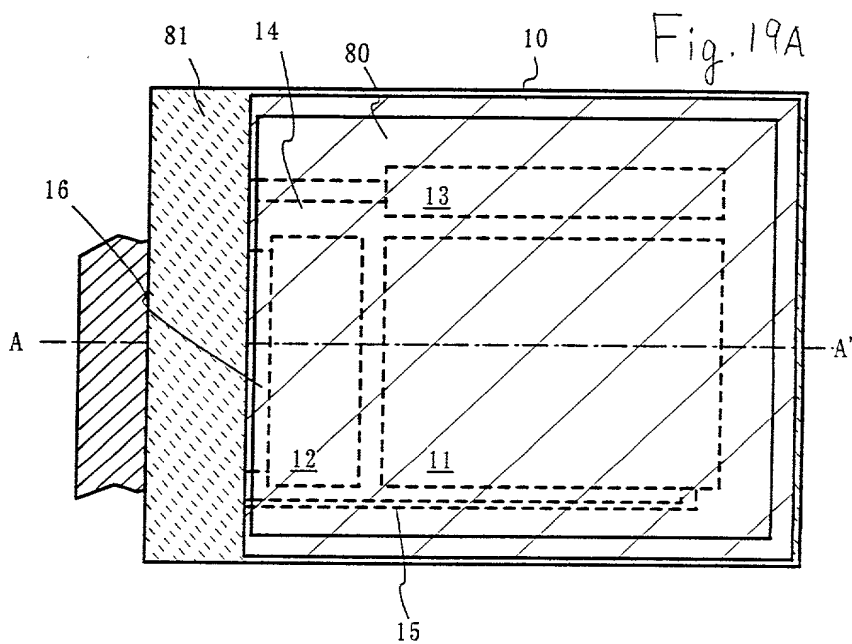


FIG. 20A is a cross-sectional view of a pixel structure in a display device, showing a switching TFT 2402, an EL element 2405, and a current controlling TFT 2403. The structure includes a substrate 36, a switching TFT 2402 with gate 35 and source/drain 39a, 39b, and a current controlling TFT 2403 with gate 40 and source/drain 42, 44a, 44b. An EL element 2405 is positioned between the TFTs, with a light-emitting layer 45 and electrodes 41, 43. An arrow indicates the direction of light emission.

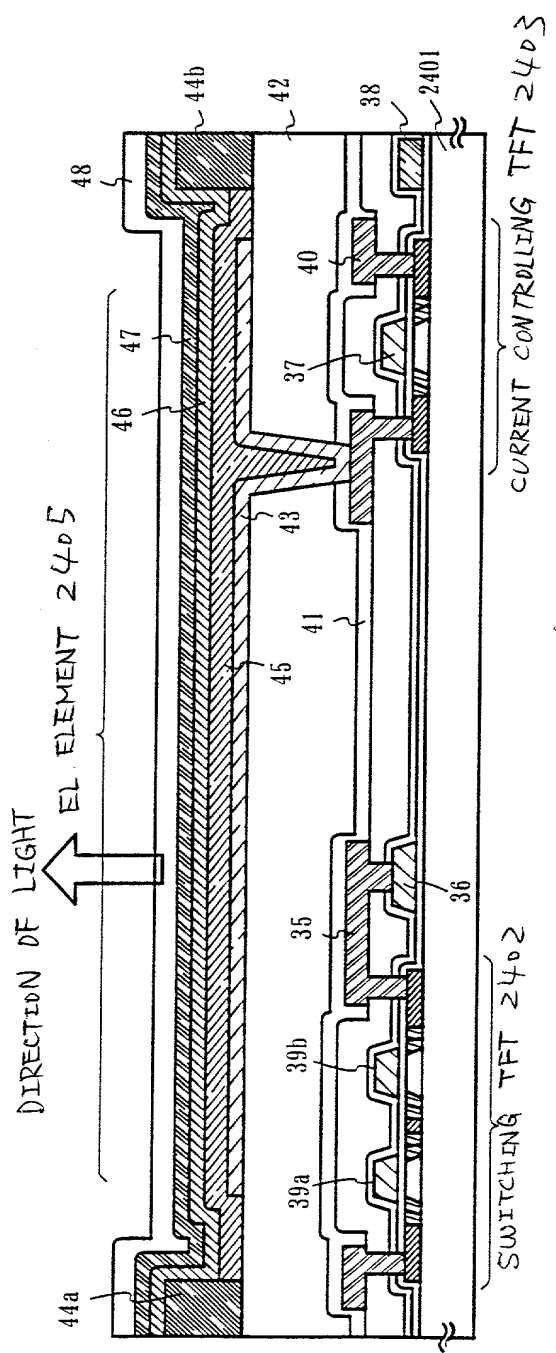


Fig. 20A

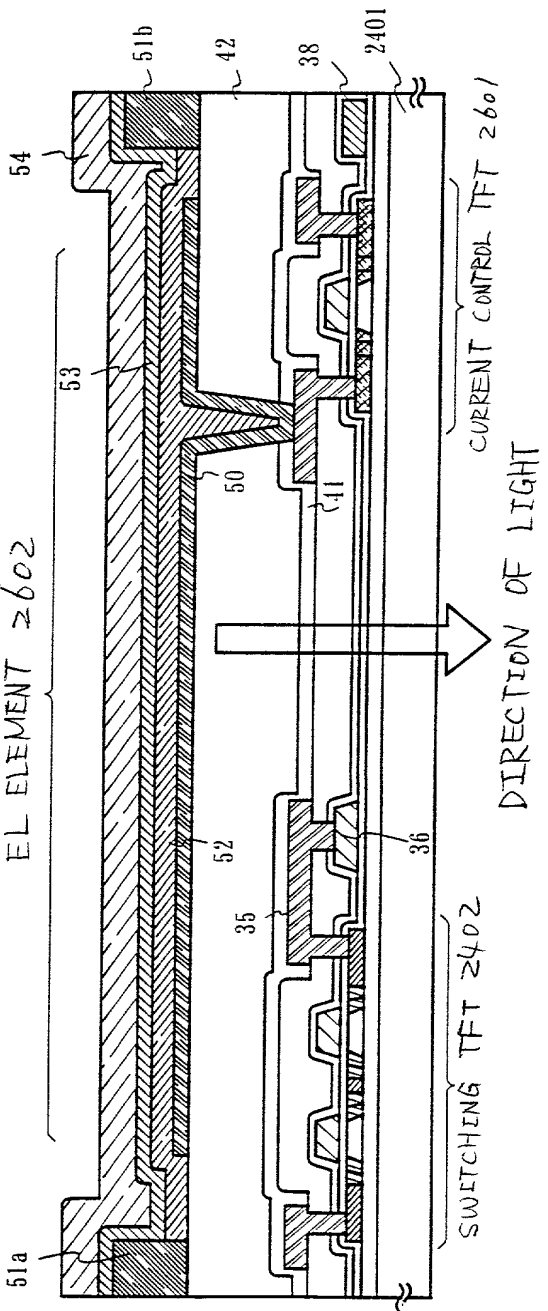
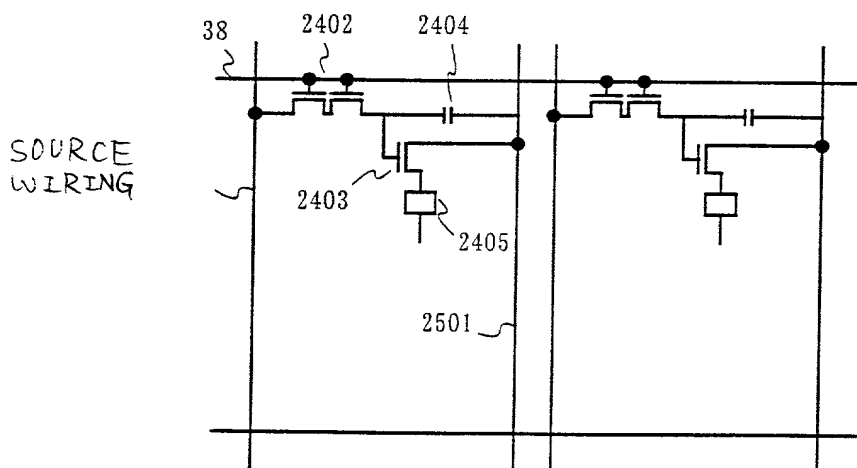
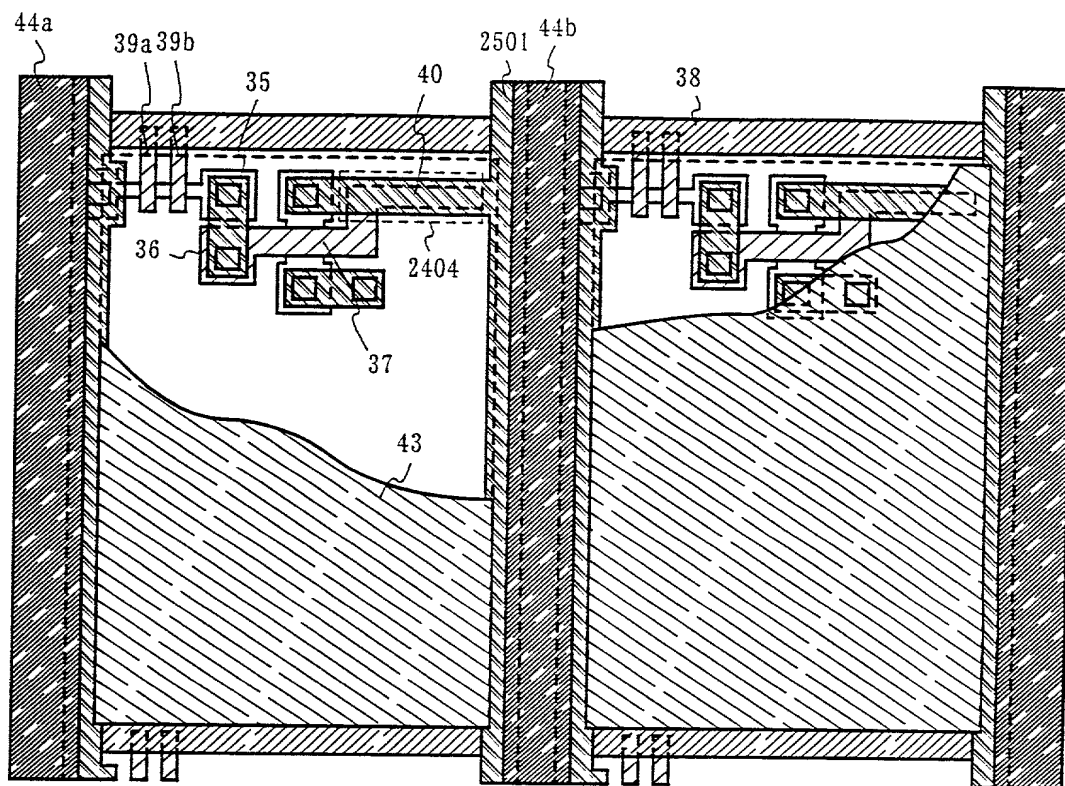


Fig. 20B



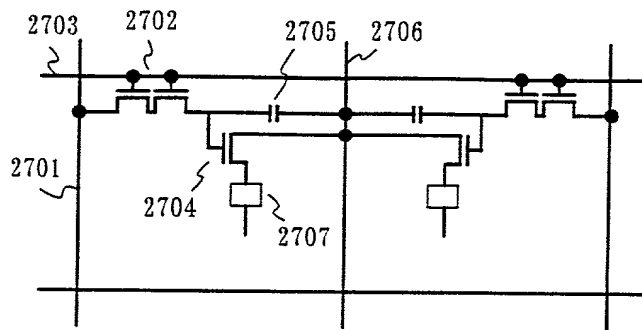


Fig. 22A

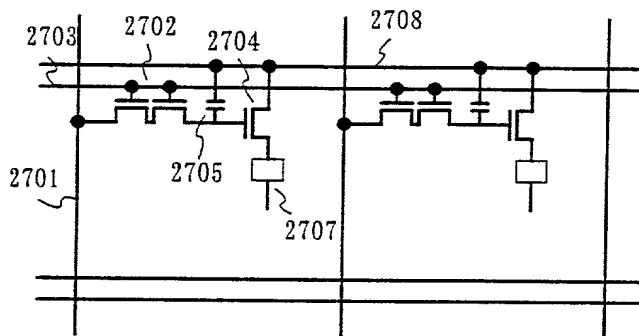


Fig. 22B

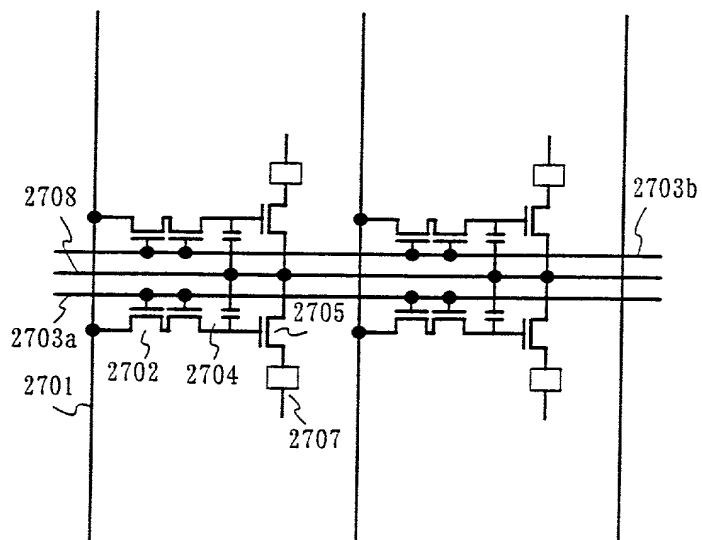


Fig. 22C

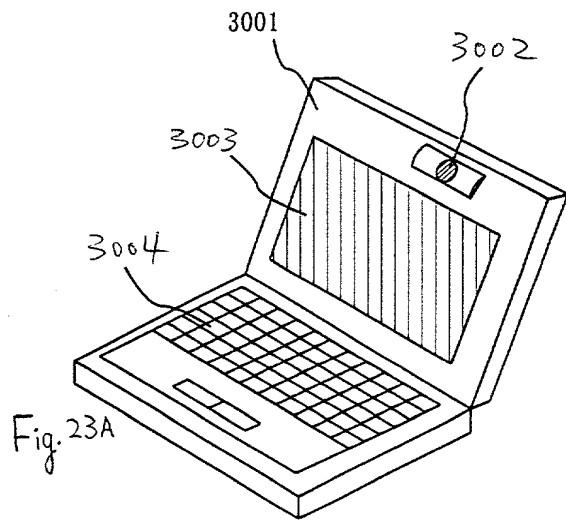


Fig. 23A

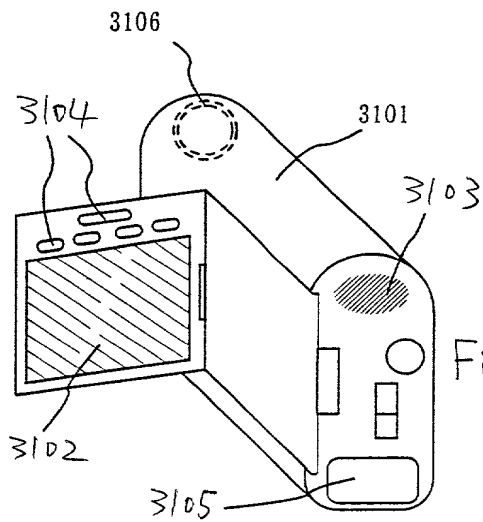


Fig. 23B

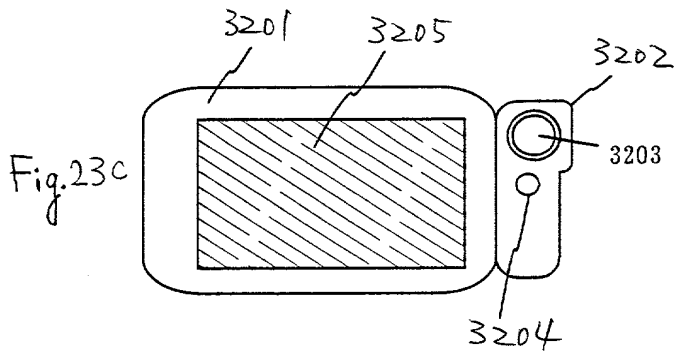


Fig. 23C

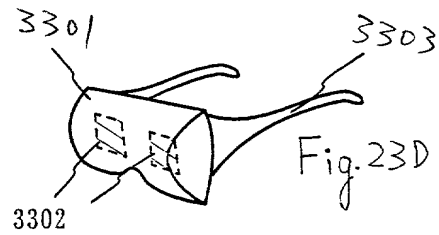


Fig. 23D

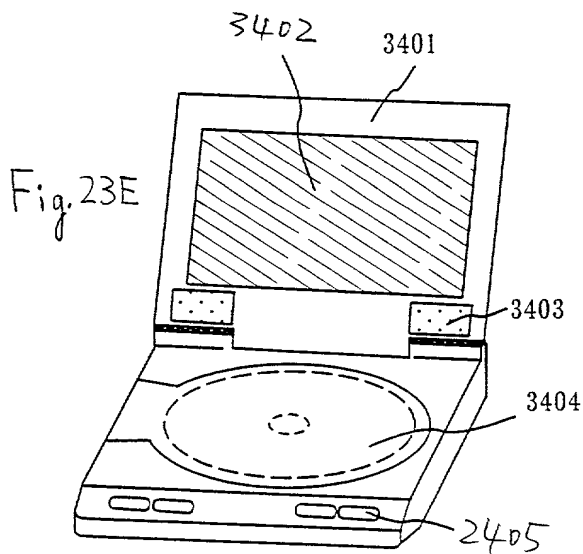


Fig. 23E

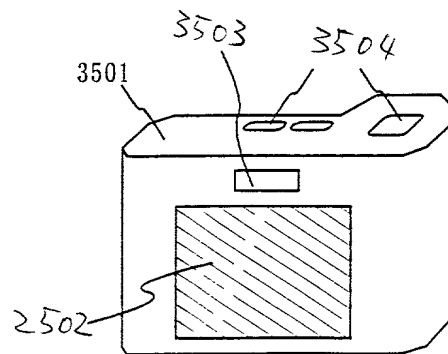


Fig. 23F

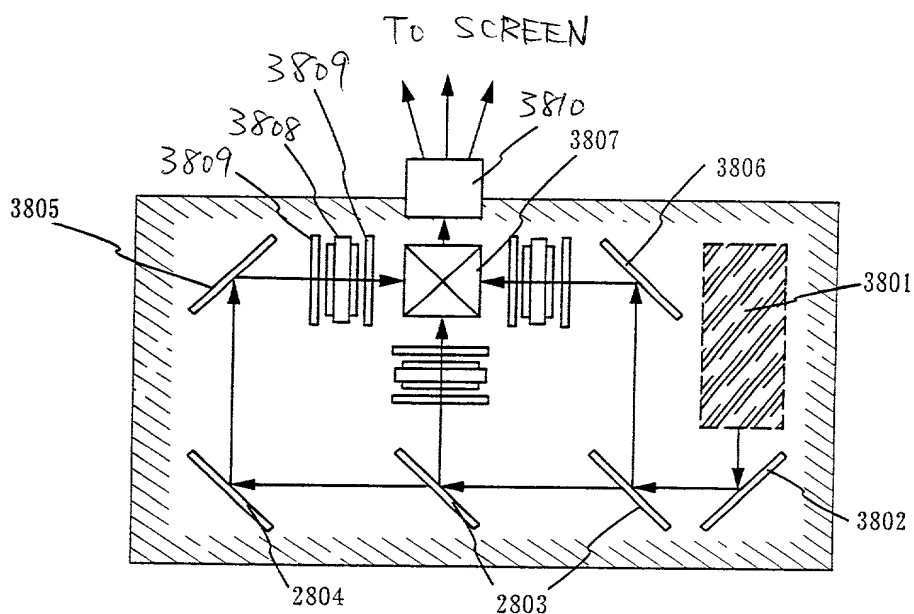
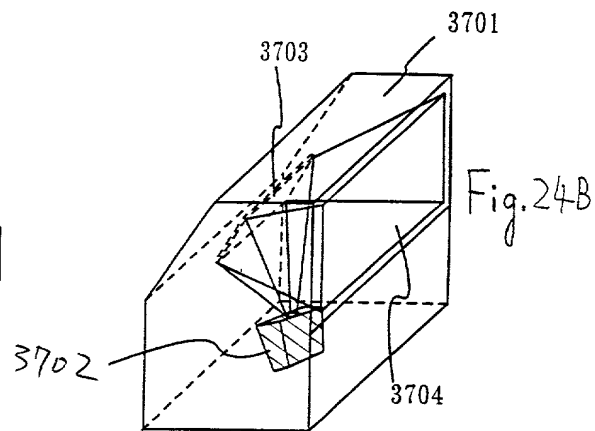
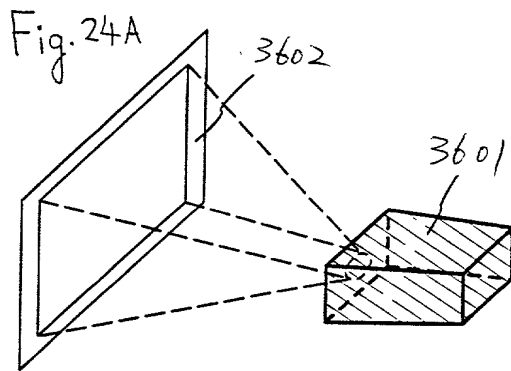


Fig. 24C PROJECTION DEVICE (THREE PLATE TYPE)

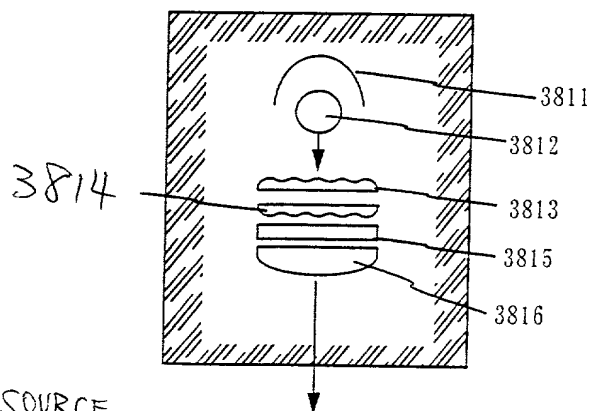


Fig. 24D LIGHT SOURCE OPTICAL SYSTEM

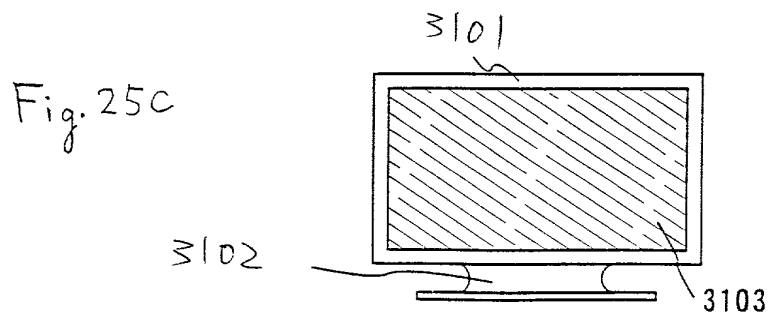
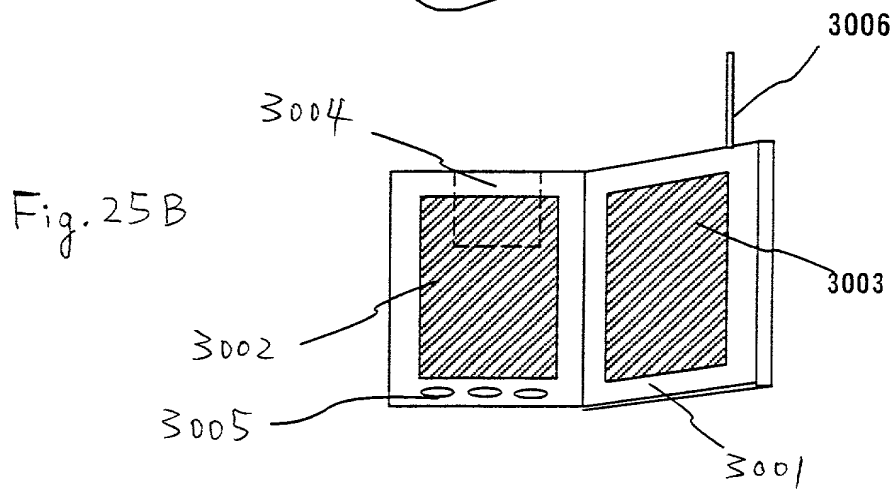
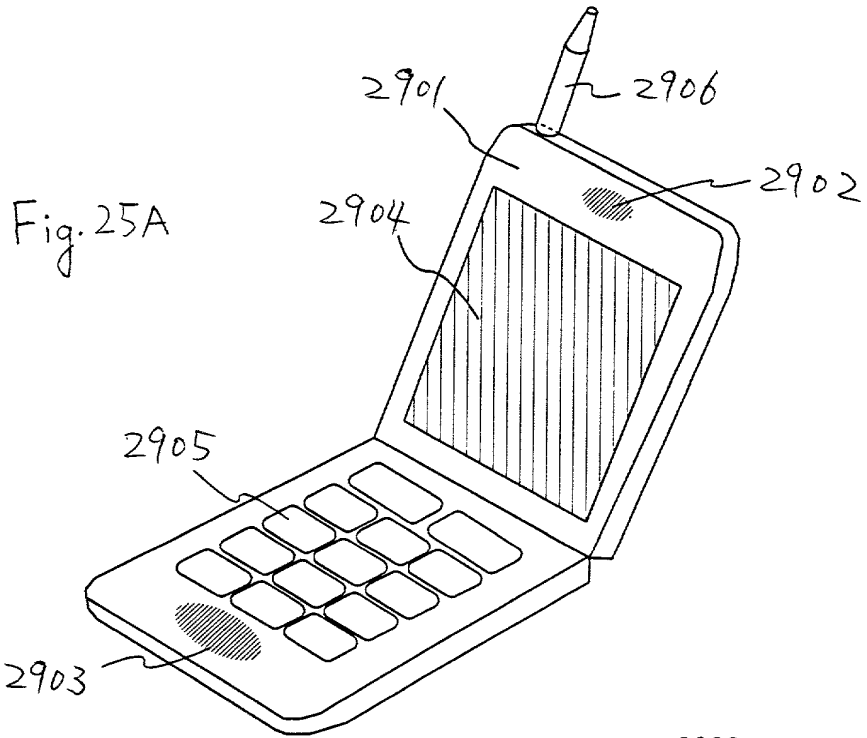


Fig. 26A

OVERLAPPED PORTION
OF LASER
IRRADIATION

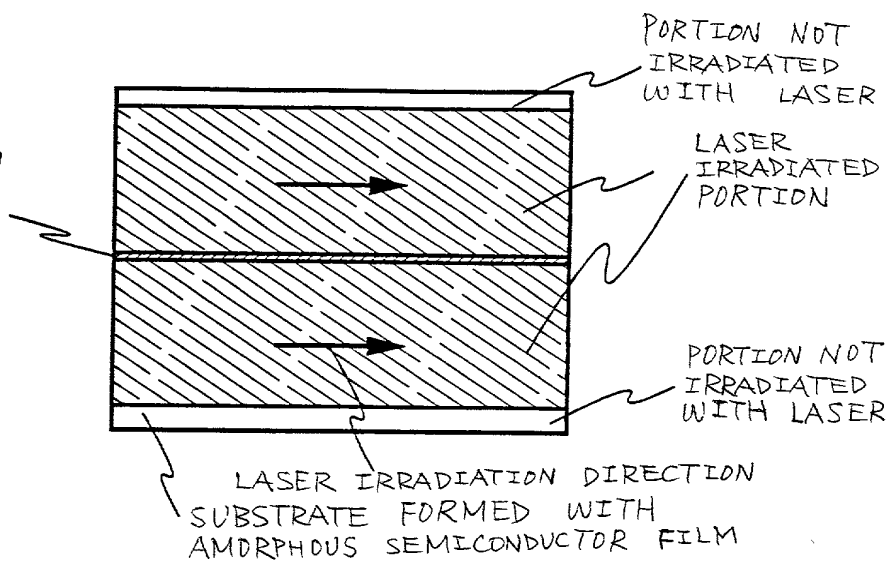


Fig. 26B

PORTION NOT
IRRADIATED
WITH LASER

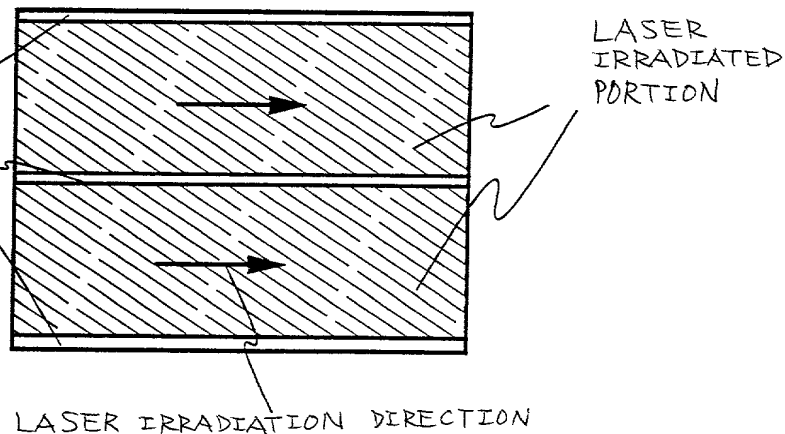


Fig. 26C

PORTION NOT
IRRADIATED
WITH LASER

